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Novak

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[54] **POWER ON, MATED, AND ACTIVITY INDICATOR FOR ELECTRONIC DEVICES INCLUDING STORAGE DEVICES**

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[51] Int. Cl.⁶ **G08B 21/00**

[52] U.S. Cl. **340/635; 340/502; 340/686; 340/687; 340/332; 395/835**

[58] **Field of Search** 340/502, 635, 340/686, 687, 331, 332, 286.11, 815.45, 945, 971; 395/835, 837, 838, 839

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[57] **ABSTRACT**

The present invention signals the power on, mated, and activity status of storage units in a console rack-mounted memory system. The status is signalled using parallel-coupled LEDs activated by logically combined signals provided by a logic circuit using signals available from SCA connector pins that connect the storage units to the console rack. The SCA connector provides MATED1 (pin 44), MATED2 (pin 74), and LED ACTIVE (pin 77) signals that are logically ANDed by the logic circuits. A logic circuit lights an associated parallel-coupled LED pair for a storage unit when $LIT = MATED \cdot ACTIVE$ LED, where $MATED = 0$ denotes grounded pins 74 and (optionally) 44, and $ACTIVE$ LED=0 denotes grounded pin 77, and $LIT=1$ denotes lit LEDs. A steadily lit LED pair denotes that a storage unit is mated into the console rack and is inactive. An intermittent LED pair denotes that the storage unit is mated into the console rack and is presently active (and thus should not be removed). If no LEDs are lit on the console-rack, then it is known that operating power is not being provided to the memory system.

23 Claims, 5 Drawing Sheets

